## **AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application:

## **Listing of Claims:**

1	1. (Currently Amended) A system design method for designing a system		
2	which includes a plurality of system components, the method comprising:		
3	defining respective functional representations of the plurality of system		
4	components, each functional representation including at least one parameter value; and		
5	automatically defining an allowable set of such parameter values in		
6	dependence upon the plurality of system components, wherein the allowable set of parameter		
7	values includes defining at least one common compatible parameter values from the respective		
8	functional representations of at least two of the plurality for of the components.		
1	2. (Original) A method as claimed in claim 1, wherein one of the system		
2	components is a bus.		
_	components is a ous.		
1	3. (Original) A method as claimed in claim 2, wherein the functional		
2	representation of the bus includes a parameter value relating to bus width.		
1	4. (Original) A method as claimed in claim 1, further comprising choosing		
2	an allowable set of parameter values and setting the parameter values of the functional		
3	representations concerned to the values defined by the chosen allowable set of parameter values		
•	(Olivinal) A weather a relative of in claims 1. Southern community the stone		
1	5. (Original) A method as claimed in claim 1, further comprising the steps		
2	of:		
3	selecting a plurality of system components;		
4	selecting a connection for interconnecting such selected system		
5	components; and		

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)	selecting one of the allowable sets of parameter values, in dependence		
7	upon said connection.		
l	6. (Currently Amended) A system component model for use in a method for		
2	designing a system comprising a plurality of system components, the model including a		
3	functional representation of the component concerned, which representation includes at least one		
1	parameter value for the component, wherein the parameter value relates to a data transfer		
5	protocol operation associated with the component.		
l	7. (Currently Amended) A model as claimed in claim 6, wherein the		
2	functional representation includes a parameter value that relates to a data transfer protocol role		
3	characteristics of the component.		
ļ	8. (Currently Amended) A model as claimed in claim 6, wherein the		
2	<u>functional representation includes a parameter value that</u> relates to a bus width.		
l	9. (Currently Amended) Apparatus for designing a system which includes a		
2	plurality of system components, the apparatus comprising:		
3	a data storage medium which is operable to store respective functional		
1	representations of a plurality of system components, each functional representation including at		
5	least one parameter value; and		
5	a processor which is operable to define automatically an allowable set of		
7	parameter values for a selected group of system components, wherein the allowable set of		
3	parameter values includes at least one common parameter value from the respective functional		
)	representations of at least two of the plurality of the components.		
l	10. (Original) Apparatus as claimed in claim 9, wherein one of the system		
2	components is a bus.		
ı	11. (Currently Amended) Apparatus as claimed in claim 10, wherein the		
l •	functional representation of the bus includes a parameter value relating to bus width.		
,	. Indictional representation of the bus includes a parameter value relating to bus within.		

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1	12.	(Original) Apparatus as claimed in claim 9, wherein the processor is
2	operable to choose an	n allowable set of parameter values and setting the parameter values of the
3	functional representa	tions concerned to the values defined by the chosen allowable set of
4	parameter values.	
1	13.	(Original) Apparatus as claimed in claim 9, wherein the processor is
2	operable to:	
3		select a plurality of system components;
4		select a connection for interconnecting such selected system components;
5	and	
6		select one of the allowable sets of parameter values, in dependence upon
7	said connection.	·
1	14.	(Original) A programmable logic device designed in accordance with a
2	method as claimed in	claim 1.
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1	15.	(Original) A programmable logic device designed using apparatus as
2	claimed in claim 9.	